

**AMENDMENT TO THE CLAIMS**

**Listing of Claims:**

1. (Currently Amended) A method for executing a ~~predetermined process in a SMM predefined SMM-compatible task~~ while operating under ACPI control, comprising:
  - generating a SMI request under ACPI control;
  - changing an operation mode of a processor to the SMM in response to the SMI request; and
  - executing a resume ACPI control process to return the processor to ACPI control after the predefined SMM-compatible task process is complete.
2. (Original) The method according to claim 1, wherein changing the operation mode of a processor to the SMM further comprises:
  - saving processor state map information in a first area of a memory upon reception of the SMI request.
3. (Original) The method of claim 1, wherein generating a SMI request under ACPI control further comprises:
  - setting SMI enable in a SMI generation register.
4. (Currently Amended) The method of claim 1, wherein generating a SMI request under ACPI control further comprises:
  - detecting ~~at~~the predefined SMM-compatible task under ACPI control; and
  - configuring the processor to access a SMI driver upon detecting the predefined SMM-compatible task.
5. (Currently Amended) The method of claim 4, wherein configuring the processor to access the SMI driver upon detecting the predefined ~~SMI-~~SMM-compatible task ~~comprising~~comprises:
  - using a SMI generation register for generating ~~at~~the SMI request during operation of an operating system under ACPI control in response to detection of ~~at~~the predefined SMM-compatible task.
6. (Currently Amended) The method of claim 4, wherein ~~the~~a SMI generation register is configured to generate an SMI signal in response to recognition of ~~an~~the predefined SMM-compatible task.

7. (Currently Amended) A method for executing tasks in SMM ~~a predefined SMM-compatible task~~ under ACPI control, comprising:

generating a SMI request during operation of an operating system under ACPI control;  
changing a mode of a processor to SMM in response to the SMI request;  
executing tasks by the processor in the SMM mode;  
delegating tasks to various processors to achieve faster execution in SMM mode;  
and  
returning the processor to ACPI control ~~after the predefined SMM-compatible task is complete.~~

8. (Original) The method of claim 7, wherein generating a SMI request under ACPI control further comprises:

setting SMI enable in a SMI generation register.

9. (Original) The method of claim 7, wherein changing a mode of a processor to SMM in response to the SMI request further comprises:

saving processor state map information in an area of memory upon reception of the SMI request, and changing the mode of the processor to the SMM.

10. (Currently Amended) The method of claim 7, wherein generating a SMI request under ACPI control further comprises:

detecting ~~at the~~ predefined SMM-compatible task under ACPI control; and  
configuring the processor to access a SMI driver upon detecting the predefined SMM-compatible task.

11. (Currently Amended) The method of claim 10, wherein configuring the processor to access the SMI driver upon detecting the predefined ~~SMI-SMM~~-compatible task comprising:

using a SMI generation register for generating ~~at the~~ SMI request during operation of an operating system under ACPI control in response to detection of ~~at the~~ predefined SMM-compatible task.

12. (Currently Amended) The method of claim 10, wherein ~~the~~ SMI generation register is configured to generate an SMI signal in response to recognition of ~~an~~~~the~~ predefined SMM-compatible task.

13. (Currently Amended) A machine readable medium having stored therein a plurality of machine readable instructions executable by a processor to execute a predefined SMM-compatible task in SMM while under ACPI control, comprising:

instructions to generate a SMI request during operation of an operating system under ACPI control;

instructions to change a mode of a processor to SMM in response to the SMI request;

instructions to execute tasks by the processor in the SMM mode; and

instructions to return the processor to ACPI control after the predefined SMM-compatible task is complete.

14. (Original) The machine readable medium of claim 13, wherein instructions to generate a SMI request during operation of an operating system under ACPI control further comprises:

instructions to set SMI enable in a SMI generation register.

15. (Original) The machine readable medium of claim 13, wherein instruction to change a mode of a processor to SMM in response to the SMI request further comprises:

instructions to save processor state map information in an area of memory upon reception of the SMI request, and changing the mode of said processor to the SMM.

16. (Currently Amended) The machine readable medium of claim 13, wherein instructions to generate a SMI request during operation of an operating system under ACPI control further comprises:

instructions to detect at the predefined SMM-compatible task under ACPI control; and

instructions to configure the processor to access the SMI driver upon detecting the predefined SMM-compatible task.

17. (Currently Amended) The machine readable medium of claim 16, wherein instructions to configure the processor to access the SMI driver upon detecting the predefined SMI-SMM-compatible task further comprises:

instructions to generate a the SMI request during operation of an operating system under ACPI control in response to detection of a the predefined SMM-compatible task.

18. (Currently Amended) The machine readable medium of claim 16, further comprising:

instructions to configure ~~thea~~ SMI register to generate a SMI signal in response to recognition of ~~at~~ the predefined SMM-compatible task.